

RF Pad Library

1 Features

- This IP is a conglomeration of following sub blocks:
- Small pads with low parasitic capacitance to ground
- Large pads to allow double bonding for reduced impedance
- Includes both the analog and digital bidirectional pads
- ESD structures included
- 1.8 V Driver Supply Voltage

2 Technology

TSMC 0.18 μ m Logic, TSMC 90nm Logic

3 General Description

Wipro-NewLogic's RF Pad Library has been designed to increase the high frequency performance of pads used in RF applications. The RF Pads have smaller size and very low parasitic capacitance. This improves IC performance in high frequency RF applications and can provide smaller die sizes. Pads designed to allow multiple bonding to reduce impedance are also provided. The Pads are particularly suitable for RF applications working in the range of 2.4 GHz to 5 GHz (RF pads) and 200 MHz of switching digital signals (digital pads)

The Pads include ESD structures which are designed to prevent the core circuits from serious damage due to an ESD event.

4 Block Diagram

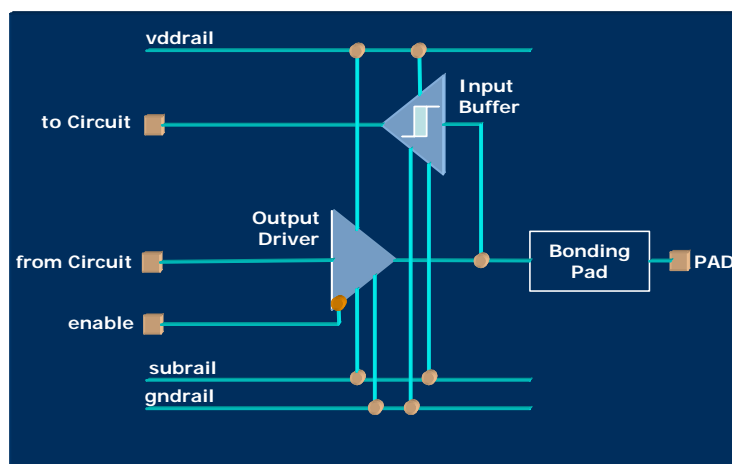


Figure 1: Functional Block Diagram



5 Deliverables

- Release Notes
- GDSII
- Basic AHDL Simulation models
- Datasheet and Documentation
- Application Note and Test Guidelines

6 Applications

Suitable for all wireless applications ranging upto 5 GHz including Wireless LAN, Bluetooth, WiMAX etc.

For more details about our products and services, please contact us at semi.ip@wipro.com.

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